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PTO-1449 (Modified) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT	ATTY. DOCKET NO. RA001C10	SERIAL NUMBER 09/514,872
	APPLICANT(S) FARMWALD ET AL.	
	FILING DATE FEBRUARY 28, 2000	GROUP ART UNIT ~~2781~~ 2181

U.S. PATENT DOCUMENTS

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	Ikeda, Hiroaki et al., "**100 MHz Serial Access Architecture for 4Md Field Memory**," Symposium of VLSI Circuits, Digest of Technical Papers, pp. 11-12 (Jun. 1990)
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